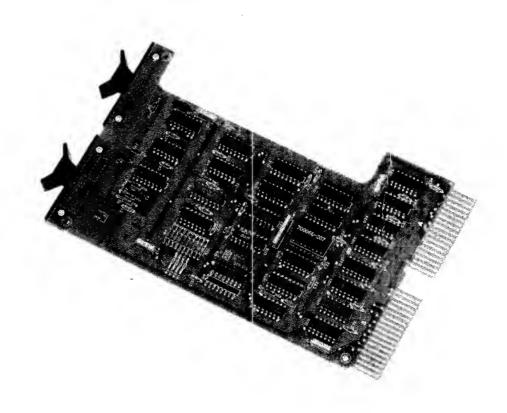
PM-RL11 and PM-RL11B BOOTSTRAP LOADER for the PDP-11





PM-RL11 and PM-RL11B Bootstrap Loader for the PDP-11

GENERAL DESCRIPTION

The PM-RL11 and PM-RL11B are bootstrap loaders that permit fast loading of bootstrap programs or restarting the DEC PDP-11 programs for such devices as paper tape readers, discs, magnetic tape, DECtape.* The general purpose loaders are designed for use in the PDP-11 system with at least 4K of memory and one or more bulk storage devices. It may be placed in any slot that is wired to the UNIBUS, e.g., slots A and B of a memory location or UNIBUS slots A3 and B3 of the Plessey PM-DC11 disc controller. The bootstrap programs are contained in a read-only memory (ROM).

The PM-RLll works with a switch register and contains bootstraps for the following devices:

- DEC TCll DECtape Magnetic Tape System
- DEC RF11 Moving Head Disc System
- DEC RCll Moving Head Disc System
- DEC RK11 or Plessey PM-DS11 Moving Head Disc System
- DEC RP11 or Plessey PM-DS11/14 Moving Head Disc System

The PM-RL11B works with a console and bootstraps all the devices listed above for the PM-RL11 plus the DEC TM11 or Plessey PM-TS11 Magnetic Tape System and the DEC RX11 or Plessey PM-XS11 Floppy Disc Systems.

FEATURES

- General purpose bootstrap loader for all PDP-11 systems
- Contains bootstrap loaders for the most frequently used PDP-11 devices

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LOADING PROGRAMS

The PM-RL11 and PM-RL11B bootstrap loaders are shipped with jumper wires connected for starting address 773000. Its ROM locations are pre-programmed for a bulk storage (disc or DECtape) bootstrap loader programs and a paper tape bootstrap loader program.

The switch register operation of the PM-RL11 utilizes device addresses, and the console operation of the PM-RL11B uses device codes as follows:

					RL11B
		DEVICE CSR	STARTING	ADDRESS	CONSOLE
DEC	PLESSEY	ADDRESS	RL11	RL11B	MNEUMONICS
				-	
TMll	PM-TS11	772520	N/A	773000	MT
TS11	N/A	777344	773100	A	$\mathtt{D}\mathbf{T}$
RF11	N/A	777462	773100	Ī	RF
RC11	N/A	777450	773100		RC
RK11	PM-DS11	777406	773100		RK
RP11	PM-DS11/80	776716	773100		RP
RX11	PM-XS11	77 717 0	N/A	¥	RX
PCll	PM-PR11	777550	773000†	773000†	PR

†Tries high speed reader first. If none, low speed reader is selected.

PROGRAM LISTING

The program listing for the bulk storage bootstrap loader program and the paper tape bootstrap are contained the PM-RLll and PM-RLllB manuals (MA 700596-100 and MA 700596-201 respectively) which are shipped with the equipment.

RELIABILITY AND QUALITY ASSURANCE

Plessey materials, fabrication, and workmanship conform to the best commercial practices. Selected components are preconditioned prior to assembly to enhance system reliability. Assembled systems are subjected to dynamic burn-in testing at elevated temperatures and are fully computer tested for proper operation using worst case diagnostics. The following standards are met:

- Printed circuit boards are gold plated on the connector fingers.
- Silicon integrated circuits are dual in-line packages unless their application is prohibited by voltage swing, power dissipation or function availability.
- All assemblies having the same part number are interchangeable.
- The circuits are designed to minimize the risk of catastrophic failure propagation.
- All hardware items of the products are resistant to corrosion.
- All components are suitably derated for maximum MTBF (means time between failures).

Workmanship is consistent with the best commercial computer practices and products are designed for high reliability and maintainability as well as low cost and state of the art electrical performance.

SPECIFICATIONS

Installation

The PM-RL11 and PM-RL11B can be installed in any standard or modified Unibus slot of a PDP-11 system with at least 4K of memory and one or more bulk storage devices.

Electrical Specifications

Power Requirements: +5VDC 1A

ROM Cycle Time: 500ns

Starting Address: RL11 - 773000 or 773100

RL11B - 773000

Unibus Loading: 1 bus load

Operation: RL11 Switch register

RL11B Console

Environmental Specifications

Temperature

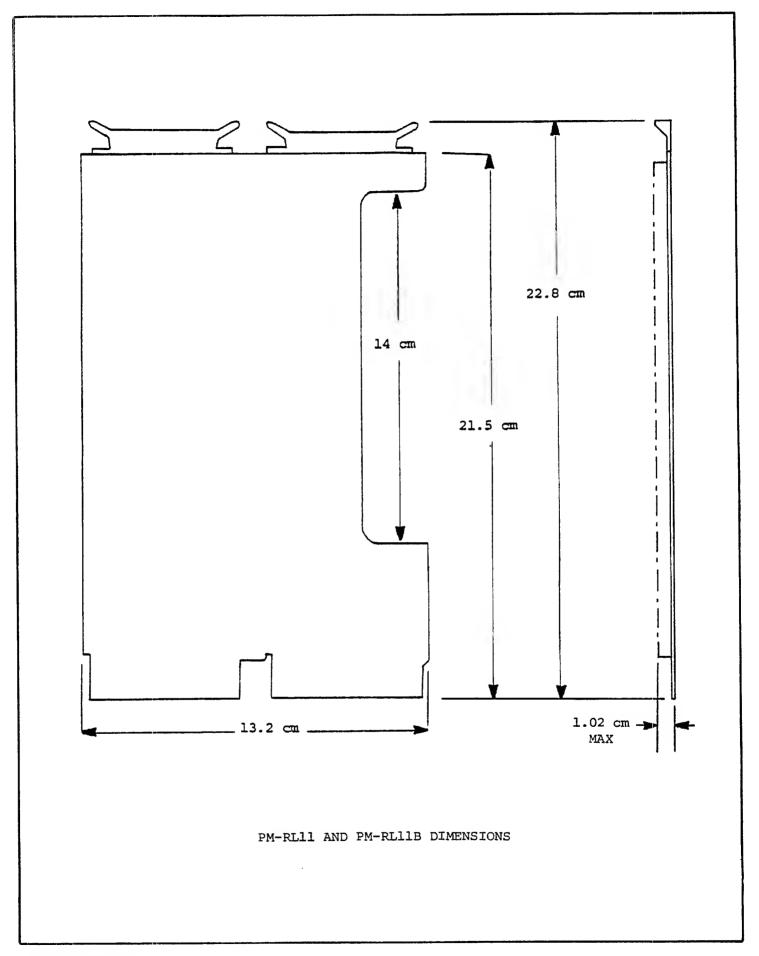
Operating: 0°C to +50°C

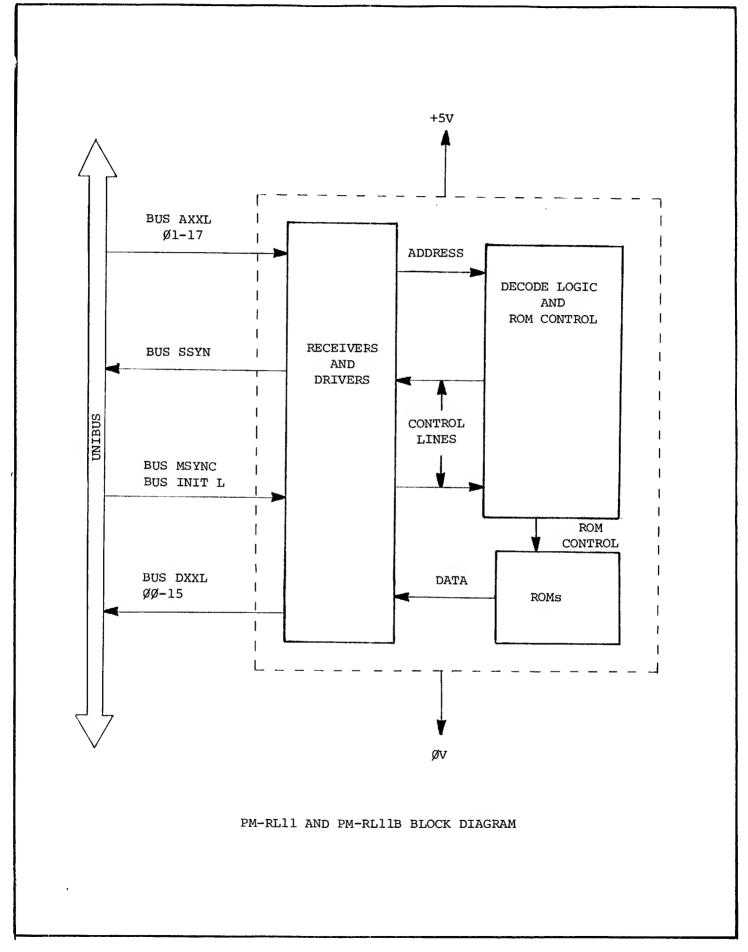
Nonoperating: -10°C to +85°C

Relative Humidity: 10% to 90% without condensation

Physical Specifications

The PM-RL11 is contained on a single dual wide printed circuit board with dimensions as shown below. It is a two-layered etch board with power, ground and logic traces on both solder and component sides of the board.





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PM-RL11 and PM-RL11B Bootstrap Loaders Manual



PM-RL11 and PM-RL11B Bootstrap Loaders Manual

August 1978 - Revision A

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MA 700596

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Preface

This manual provides the information needed to install, operate, and program the PM-RLll bootstrap loaders manufactured by Plessey Peripheral System, Irvine, California.

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Section 1 General Information

1.1 INTRODUCTION

This manual provides the information needed to install, operate and program the PM-RLllB bootstrap loaders manufactured by Plessey Peripheral System, Irvine, CA.

The material is arranged into three sections as follows:

Section l - GENERAL INFORMATION. This section contains a general description of the PM-RLll and PM-RLllB and the specifications for the bootstrap loaders.

Section 2 - INSTALLATION AND OPERATIONS. This section explains the equipment installation and operating procedures.

Section 3 - PROGRAMS. This section contains program listings for bulk storage and paper tape bootstrap programs for the PM-FL11 and PM-RL11B.

Appendix - DRAWINGS. The appendix contains the parts list, logic diagrams and assembly drawings necessary for a complete understanding of the units.

1.2 GENERAL DESCRIPTION

The PM-RL11 and PM-RL11B are bootstrap loaders that permit fast loading of bootstrap programs or restarting the DEC PDP-11 programs for such devices as paper tape readers, discs, magnetic tape, DECtape.* The general purpose loaders are designed for use in the PDP-11 system with at least 4K of memory and one or more bulk storage devices. It may be placed in any slot that is wired to the Unibus, e.g., slots A and B of a memory location or Unibus slots A3 and B3 of the Plessey PM-DC11 disc controller. The bootstrap programs are contained in a read-only memory (ROM). Figure 1-1 is a block diagram for the bootstrap loaders.

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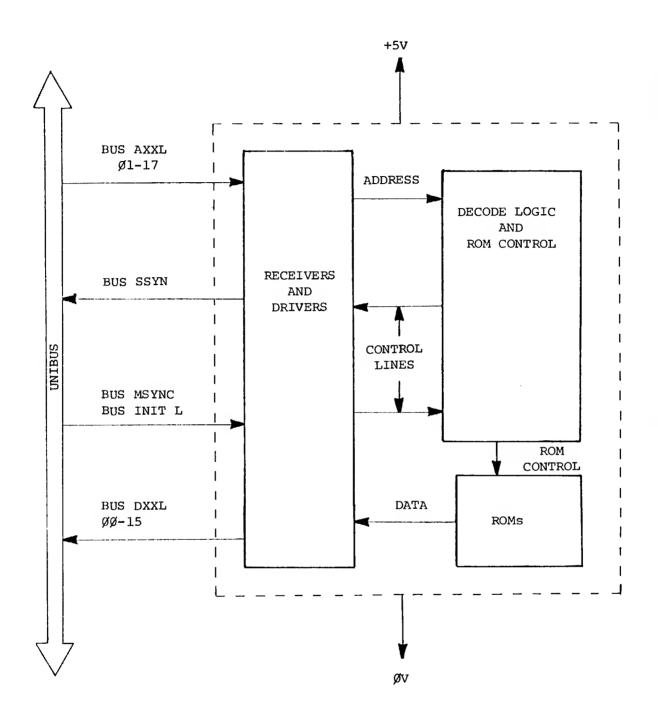


Figure 1-1: Bootstrap Loader Block Diagram

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Note that the PM-RL11 and PM-RL11B can be identified by their part numbers. The PM-RL11 is labelled P/N 700596-100 and the PM-RL11B is labelled P/N 700596-201.

The PM-RL11 works with a switch register and contains bootstraps for the following devices:

- DEC TCll DECtape Magnetic Tape System
- DEC RF11 Moving Head Disc System
- DEC RC11 Moving Head Disc System
- DEC RK11 or Plessey PM-DS11 Moving Head Disc System
- DEC RPll or Plessey PM-DSll/14 Moving Head Disc System

The PM-RL11B works with a console and bootstraps all the devices listed above for the PM-RL11 plus the DEC TM11 or Plessey PM-TS11 Magnetic Tape System and the DEC RXll or Plessey PM-Xsll Floppy Disc Systems.

1.3 LOADING PROGRAMS

The PM-RL11 and PM-RL11B bootstrap loaders are shipped with jumper wires connected for starting address 773000. Its ROM locations are pre-programmed for a bulk storage (disc or DECtape) bootstrap loader programs and a paper tape bootstrap loader program.

The switch register operation of the PM-RL11 utilizes device addresses, and the console operation of the PM-RL11B uses device codes as follows:

DEC	PLESSEY	DEVICE CSR ADDRESS	STARTING RL11	ADDRESS RL11B	RL11B CONSOLE MNEUMONICS
TM11	PM-TS11	772520	N/A	773000	MT
TS11	N/A	777344	773100	773000	DT
RF11	N/A	777462	773100	773000	RF
RCll	N/A	777450	773100	773000	RC
RKll	PM-DS11	777406	773100	773000	RK
RP11	PM-DS11/80	776716	773100	773000	RP
RX11	PM-XS11	777170	N/A	773000	RX
PCll	PM-PR11	777550	773000	773000†	PR

†Tries high speed reader first. If none, low speed reader is selected.

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1.4 RELIABILITY AND QUALITY ASSURANCE

Plessey materials, fabrication, and workmanship conform to the best commercial practices. Selected components are preconditioned prior to assembly to enhance system reliability. Assembled systems are fully computer tested for proper operation using worst case diagnostics. The following standards are met:

- Printed circuit boards are gold plated on the connector fingers.
- Silicon integrated circuits are dual in-line packages unless their application is prohibited by voltage swing, power dissipation or function availability.
- All assemblies having the same part number are interchangeable.
- The circuits are designed to minimize the risk of catastrophic failure propagation.
- All hardware items of the products are resistant to corrosion.
- All components are suitably derated for maximum MTBF (means time between failures).

Workmanship is consistent with the best commercial computer practices and products are designed for high reliability and maintainability as well as low cost and state of the art electrical performance.

1.5 SPECIFICATIONS

1.5.1 Installation

The PM-RL11 and PM-RL11B can be installed in any standard or modified Unibus slot of a PDP-11 system with at least 4K of memory and one or more bulk storage devices.

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1.5.2 Electrical Specifications

Power Requirements:

+5VDC 1A

ROM Cycle Time:

500ns

Starting Address:

RL11 - 773000 or 773100

RL11B - 773000

Unibus Loading:

1 bus load

Operation:

RLll Switch register

RL11B Console

1.5.3 Environmental Specifications

Temperature

Operating:

0°C to +50°C

Nonoperating: -10°C to +85°C

Relative Humidity: 10% to 90% without condensation

1.5.4 Physical Specifications

The PM-RLll is contained on a single dual wide printed circuit board with dimensions as shown below. It is a two-layered etch board with power, ground and logic traces on both solder and component sides of the board. See Figure 1-2.

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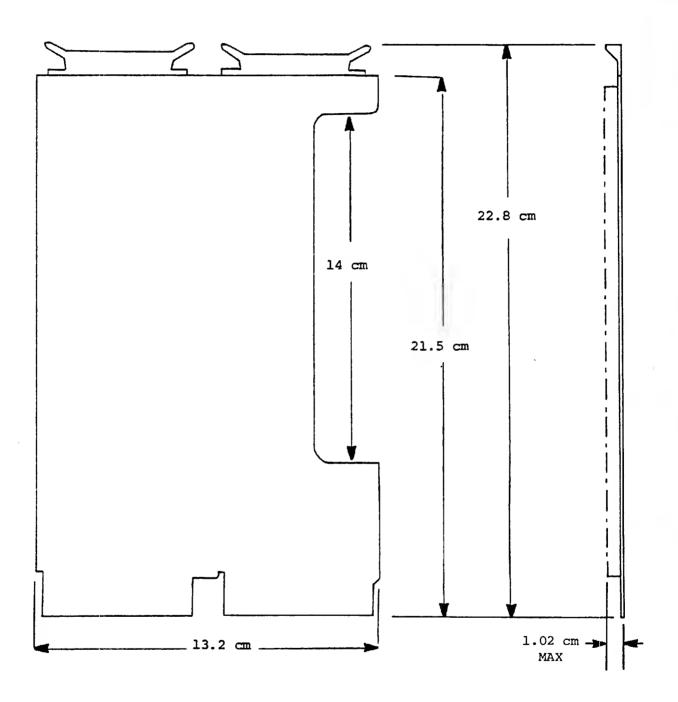


Figure 1-2: PM-RL11 and PM-RL11B Dimensions

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Section 2

Installation and Operation

2.1 UNPACKING AND INSPECTION

The PM-RL11 and PM-RL11B are shipped in a special packing carton designed to keep the board from vibrating and to give them maximum protection during shipment. The packing carton should be retained in case the unit requires reshipment.

Remove any packing materials before removing the bootstrap loader from its carton. Visually inspect for any physical damage.

2.2 INSTALLATION

The PM-RL11 and PM-RL11B plug into any Unibus or modified Unibus (MUD) location in the A-B portion of expansion slots in the following backplanes:

• Plessey PM-Dll/SPC-1

PM-D11/SPC-2 PM-F11/SPC PM-F11/SPC-1 PM-DC11 (J3 A-B)

DEC DD11-C

DD11-D DD11-P

Any other DEC backplane containing expansion slots for Unibus or MUD.

2.3 OPERATION

2.3.1 Operating Procedure for PM-RL11

The PM-RLll has separate operating procedures for bulk storage and paper tape bootstrap as follows:

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BULK STORAGE BOOTSTRAP PROCEDURE

- 1. Set the HALT/ENABLE switch to HALT, then ENABLE.
- 2. Set ROM address 773100 into the Switch Register.
- 3. Press the LOAD ADDRESS switch.
- 4. Enter into the Switch Register the device address of the disc or DECtape to be used according to Table 2-1.

DEC	DEVICE PLESSEY	DEVICE ADDRESS
TS11	N/A	777344
RF11	N/A	777462
RC11	N/A	777450
RK11	PM-DS11	777406
RP11	PM-DS11/80	776716
RX11	PM-XS11	777170

Table 2-1: Device Addresses for PM-RL11

5. Press the START switch. The disc or DECtape data should read into memory.

PAPER TAPE BOOTSTRAP PROCEDURE

- 1. Set the HALT/ENABLE switch to HALT, then to ENABLE.
- Place the absolute loader paper tape in the reader to be used, with the special tape leader placed over the read head.
- 3. If the high speed reader is to be used, set the switch to ON.
- 4. If the low speed reader is to be used, set the high speed reader switch to OFF and the low speed reader switch to START.
- 5. Set the starting address, 773000, into the SWITCH REGISTER.

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- 6. Press the LOAD ADDR switch.
- 7. Press the START switch. After a short pause the paper tape should read in.

2.3.2 Operating Procedure for PM-RL11B

The PM-RL11B has a single operating procedure for both bulk storage and paper tape bootstrap as follows:

- 1. Set the HALT/ENABLE switch to HALT, then to ENABLE.
- Load address 773000 into the CPU.
- Start the CPU at this address.
- 4. According to Table 2-2 type in the 2 letter device code of the device to be booted. NOTE: Prior to typing the 2 letter code, make sure the device to be booted is ready, or, if paper tape is used, make sure the absolute loader tape is installed in the reader. Correct absolute loader is loaded into the tape reader.

DEVICE DEC	PLESSEY	DEVICE
TM11 TS11 RF11 RC11 RK11 RP11 RX11 PC11	PM-TS11 N/A N/A B/A PM-DS11 PM-DS11/80 PM-XS11 PM-PR11	MT DT RF RC RK RP RX PR†

†Tries high speed reader first. If none, low speed reader is selected.

Table 2-2: PM-RL11B Device Codes

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Section 3 **Programs**

3.1 PROGRAMS FOR THE PM-RL11

The PM-RL11 program for the bulk storage bootstrap loader and for the paper tape bootstrap can be found in Program A.

3.2 PROGRAMS FOR THE PM-RL11B

Program B contains the listing for the bulk storage and paper tape bootstrap program for the PM-RL11B.

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; PAPER TAPE BOOTSTRAP PROGRAM

```
;ADDRESS POINTER
       000001 R1 = %1
                                                 ;TEMPORARY STORAGE
       000002 R2 = %2
                                                 ;TEMPORARY STORAGE
       000003 R3 = %3
                                                 ; DEVICE POINTER
       000004 R4 = %4
                                                ;STACK POINTER
       000006 SP = %6
                                                 ;PROGRAM COUNTER
       000007 PC = %7
                                                ;HIGH SPEED READER ADDRESS
       177550 HSR = 177550
                                                 ;LOW SPEED READER ADDRESS
       177560 LSR = 177560
       173000 . = 173000
                                                 ;SET MEMORY CHECK LIMITS
                                #160000, R1
173000 012701 START:
                       MOV
       160000
                                                 ;TRAP VECTOR IS LOCATION 4 & 6
                                #6,R2
                       MOV
173004 012702
       000006
                                                 ; POINTER TO DEVICE ADDRESSES
                                #DEV+4, R3
                       MOV
173010 012703
       173100
                                                 ;CLEAR TRAP STAATUS AT LOC 6
                                @R2
                       CLR
173014 005012
                                                 ;SET TRAP ADDRESS AT LOC 4
                                PC, -(R2)
                       MOV
173016 010742
                                                 ;SET UP STACK OUT OF THE WAY
                               PC, SP
                       MOVB
173020 110706
                                                 GET DEVICE ADDRESS
                                -(R3),R4
                       MOV
173022 014304 DEV1:
                                                 CHECK AVAILABILITY OF DEVICE
                                CR4
                       TST
173024 005714
                                                 ;BR IF HSROUT OF TAPE (BIT 15≱
                                DEV1
                       BMI
173026 100775
                                                 FRESET TRAP ADDRESS AT LOC 4
                               PC, @RZ
                       MOV
173030 010712
                                                 ;SPECIAL ADDRESS USED AS MASK
                                #24,SP
                       MOV
173032 012706
       000024
                                                 ;MEM CHK: RDR STAT ADDR MOVED
                       MOV
                                R4, -(R1)
173036 010441
                                                 ;SET R1=X7752, MASK IN SP =24
                                SF, R1
                       BIC
173040 040601
                                                 STORE OWN ADDRESS IN POINTER
                                R1, CR1
                       MOV
173042 010111
                                                 GET BYTE POINTER
                                @R1, R2
                       MOV
173044 011102 LOOP:
                                                 ; ENABLE READER
                                @R4
                       INC
173046 005214
                                                 ;TEST DONE BIT (7)
                       TSTB
                                @R4
173050 105714
                                                 ;WAIT UNTIL READY
                                -2
                       BPL
173052 100376
                                                 ;THEN PICK IT UP AND STORE IT
                                2(R4), @R2
                       MOVB
173054 116412
       000002
                                                 ; BUMP POINTER
                       INC
                                CR1
173060 005211
                                                 ;STORED JUMP OFFSET?
                                R2, #375
                       CMPB
173062 120227
       000375
                                                 ;NOT YET
                                LOOP
                       BNE
173066 001366
                                                 ; YES, ALL DONE
                                (R2)+
                       INCB
173070 105222
                                                 ;GO EXECUTE AS BRANCH
                                -(R2)
                       JMP
173072 000142
              ;DEVICE ADDRESSES FOLLOW - DO NOT CHANGE THE ORDER
                                                 ;LOW SPEED READER
173074 177560 DEV:
                       LSR
                                                 HIGH SPEED READER
                       HSR
173076 177550
                       . END
       000001
                                  PROGRAM A
```

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.MAIN. MACRO VO6-04A 05-FEB-86 00:00 PAGE 1

; BULK STORAGE BOOTSTRAP LOADER PROGRAM

;REGISTER ASSIGNMENTS 000000 R0=%0 000001 R1=%1

173100 . = 173100

a mental and a	010701	kara I	A4177678 D1	; READ SWITCH REGISTER FOR
1/3100) 013701 177570	MOV	@#177570,R1	THE SWITCH REGISTER FOR
173104	000005 BEGIN:	RESET		FORCE CLEAR IF RETRY
	010100	MOV	R1, R0	DEVICE WORD COUNT ADDR
	012710	MOV	#-256. , @RO	;SET TO READ 256 WORDS
	177400	,		
173114	020027	CMP	RO, #177344	;IS IT DECTAPE?
	177344			
173120	001007	BNE	START	;NO, GO TO START
173122	012740	MOV	#4002, -(RO)	;YES, MOVE TAPE TO FRONT
	004002			
173126	005710	TST	@RO	;WAIT FOR ERROR
	100376	BPL	2	
	005740	TST		; IS IT ENDZONE?
	100363	BFL		;NO, TRY AGAIN
173136	022020	CMP	(RO)+, (RO)+	;ADJUST POINTER
			gramma a general s	- START ASTUAL REAR
173140		MOV	#5, -(RO)	;START ACTUAL READ
470488	000005	T-5 T D	600	· HAIT FOR DONE
	105710		@RO	;WAIT FOR DONE
	100376		2	• =====================================
	005710		m	;ERROR? ;IF SO, TRY AGAIN
	100754			
	105010			FOR DECTAPE, STOP MOTION
1/3100	000137 000000	JMP	@# 0	;GO TO ROUTINE LOADED
	000000			
	000001	. END		
	000001	m prompt A way		

PROGRAM A

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SIZE CODE IDENT NO.

MA 700596

SCALE REV A SHEET 3-3

BOOTSTRAP-RL11 MACRO VO6-04A 11-AUG-77 00:00 PAGE 1

000000	R0=	%0
000001	R1=	7.1
000002	R2=	%2
000003	R3=	%3
000004	R4=	% 4
000005	R5=	75
000006	R6=	7.6
000007	R7=	%7
177560	TKS=	177560
177562	TKB=	177562
177564	TPS=	177564
177566	TPB=	177566
172524	BC =	172524
	പരത —	177550
177550	HSR =	
177560	LSR =	177560

173000 .= 173000

PROGRAM B

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I	SIZE	CODE IDENT NO.	DWG	NO.
	Α	52648	.	MA 700596
I	SCALE	REV	Δ	SHEET 3-4



BOOTSTRAP-RL11 MACRO V06-04A 11-AUG-77 00:00 PAGE 2

173000 0000 173002 0050 173004 0050 173006 0050 173010 0051 173012 0127 0001	000 002 003 03 01	RESET CLR CLR CLR COM MOV	RO R2 R3 R3 #122,R1	;RESET ;CLEAR RO ;CLEAR R2 ;CLEAR R3 ;MAKE R3 = 1/S ;PUT 122 IN R1
173016 0607 173020 1057 1775	01 3 7 1\$:	ADD TSTB	R7,R1 @#TPS	;FORM ADDRESS OF PROMPT ;PRINTER BSY?
173024 1003 173026 1111 1775	75 37	BPL MOVB	1\$ (R1),@#TPB	;YES, BRANCH BACK ;OUTPUT
173032 0052 173034 1203 173036 0013 173040 10573	01 11 70 37 2 \$:	INC CMPB BNE TSTB	R1 R3,(R1) 1\$ @#TKS	;INC R1 BY ONE ;ARE WE AT NULL YET? ;NO, BRANCH BACK ;YES! IS CHAR INPUT?
173044 1003 173046 11376 1775	75 00	BPL MOVB	2\$ @#TKB,RO	;NO, BRANCH BACK ;YES, PUT CHAR IN RO
173052 1057: 17756	37 3\$:	TSTB	@#TPS	;PRINTER BUSY?
173056 1003	75	BPL	3\$;YES, BRANCH BACK
173060 11000 1775	37 66	MOVB	RO,@#TPB	;NO, ECHO
173064 04276 17760	00	BIC	#177600,R0	STRIP JUNK
173070 05000 173072 00030 173074 10570 173076 00170 173100 00530 173102 00137 173104 06270	02 02 02 60 03 4\$: 76	BIS SWAB TSTB BEQ DEC BNE ADD	RO, R2 R2 R2 2\$ R3 4\$ #1, R1	; PUT CHAR INTO R2 ; SWAP BYTES ; BOTH CHAR YET? ; NO, BRANCH BACK ; WAIT A WHILE ; DONE YET? ; YES! MAKE R1 EVEN
173110 01010 173112 00573 173114 00173 173116 02023 173120 00140 173122 06270	03 11 5 \$: 31 11 03 01		R1,R3 (R1) START R2,(R1) 6\$ #6,R1	CK FOR LAST IN TABLE YES NOT IN ROM, BR BACK CHECK FOR MATCH MATCHED, CONTINUE ON NO MATCH, SET UP NEXT COMPARE
173126 00007 173130 00572 173132 01110 173134 00572 173136 06110 173140 00011 173142 00000	71 21 6 \$: 04 21 03 13	BR TST MOV TST ADD JMP HALT	5\$ (R1)+ (R1),R4 (R1)+ (R1),R3 (R3) PROGRAM B	;BRANCH BACK ;MOVE PNTR UP ONE ADDR ;MOVE CSR TO R4 ;MOVE PNTR UP ONE ADDR ;ADDR OFFSET TO R3 ;JUMP TO IT ;SOMETHING IS WRONG ;SHOULDN'T GET HERE EVER

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SIZE Д

CODE IDENT NO. DWG NO.

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SCALE

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SHEET 3-5

BOOTSTRAP-RL11 MACRO V06-04A 11-AUG-77 00:00 PAGE 3

173144 000000 TA	BLE: .WORD	0	; NULL
173146 000015	. WORD	15	; CR
173150 000000	. WORD	0 0 12 0	; NULL
173152 000000	. WORD	0	; NULL
173154 000012	. WORD	12	;LF
173156 000000	. WORD	0	; NULL
173160 000000	. WORD	0	; NULL.
173162 177452	. WORD		; *
173164 122 TBI	L1: .ASCII	/RC/	;RC
173165 103			
173166 177450	. WORD		RC WCR
173170 000110			RC OFFSET
173172 122	.ASCII	/RF/	; RF
173173 106			
173174 177462	. WORD	177462	FRF WCR
173176 000110	. WORD		RF OFFSET
173200 122	.ASCII	/RK/	; RK
173201 113			- m/2 1/0m
173202 177406	. WORD	177406	RK WCR
173204 000110	. WORD	MAIN-TBL1	;RK OFFSET ;RP
173206 122	. ASCII	/RP/	FRP
173207 120	HOOD	17/714	FRP WCR
173210 176716	. WORD	176714 MAIN-TBL1	;RP OFFSET
173212 000110	.WORD .ASCII	/DT/	;DT
173214 104 173215 124	. ASCII	7017	7 D 1
	. WORD	177040	;DT CSR
173216 177342 173220 000062	. WORD		DT OFFSET
173220 000062	. ASCII	/MT/	;MT
173223 124	. MJCII	71117	7111
173223 124	. WORD	172522	;MT CSR
173224 172322	. WORD		;MT OFFSET
173230 122	. ASCII	/RX/	;RX
173230 122	• HOW I	YIXX	711/4
173232 177170	. WORD	177170	;RX CSR
173234 000314	. WORD		RX OFFSET
173234 000314	.ASCII	/PR/	; PR
173237 122	INCOLL	r + 1 + 1 + 1	
173240 000000	. WORD	0	
173242 000214	. WORD	Ž14	;PR OFFSET
173244 000000	. WORD	Ö	FEND OF TABLE
*/ UZ TT UUUUU	1 110,10	~	4 ACCUSES A 2 SERVE AND A 2 SERVE AND AND ADDRESS AND

PROGRAM B

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BOOTSTRAP-RL11 MACRO V06-04A 11-AUG-77 00:00 PAGE 4

	000005		RESET		; RESET
1/3250	012714		MOV	#4003,(R4)	FREWIND
170054	004003		***		
	005714 100376		TST	(R4)	; IS DEC TAPE READY?
	005744		BPL	7\$	NO, BRANCH BACK
			TST	-(R4)	POINT R4 TO ERR REG
	005714		TST	(R4)	CK FOR END ZONE
	100402		BMI	8\$; YES BRANCH
	005724		TST	(R4)+	;NO, POINT R4 TO CSR
	000766		BR	DT	TRY AGAIN
	022424		CMP	(R4)+,(R4)+	POINT R4 TO WCR
1/32/4	012705		MOV	#5, R5	;PUT READ CMD IN R5
170000	000005		DE0ET		
	000005	MAINA:	RESET		FRESET
1/3302	012714		MOV	#177400,(R4)	; MOVE WORD CNT TO WCR
170007	177000		~~~		
	005744		TST	-(R4)	SET UP R4 TO POINT TO CSR
	010514		MOV	R5, (R4)	START DEVICE
	105714	75:	TSTB	(R4)	TEST FOR DONE
	100376		BPL	9\$;NO JUMP BACK
	005714		TST	(R4)	;YES TEST FOR ERROR
	100024		BPL	MT1	;NO, JUMP TO PGM START
	005724		TST	(R4)+	; YES, POINT R4 BACK TO WCR
1/3324	020427		CMP	R4, #177342	;IS THIS DEC TAPE?
170000	177342		neo.	5.7	
	001746		BEQ	DT	;YES, BACK TO DT
1/3332	020427		CMP	R4,#172522	; IS THIS MAG TAPE?
170004	172522		Dire	MATHA	
	001360 000005	MT.	BNE	MAINA	;NO, BACK TO MAINA
	005137	m i s	RESET	200	RESET
1/3342			COM	BC	; PUT ALL 1'S IN BYTE COUNT
172244	172524 012714		MOU	#/0044 /DA	- 05 400 - 500 1400
1/3346	060011		MOV	#60011, (R4)	;SPACE FORWARD
172252	105714	104.	TOTO	(D4)	. 70 70 0000
	100376	10*.	TSTB BPL	(R4)	; IS IT DONE?
173354	005714		TST	10\$	NO, BRANCH BACK
	100767			(R4)	CHECK FOR ERROR
	005724		BMI	MT	; ERROR! BRANCH BACK
	012705		TST	(R4)+	MOVE PATE TO NEXT ADDR
1/3304	060003		MOV	#60003,R5	;PUT READ CMD IN R5
172270	000743		BR	MATNA	. MC TO MATHA
	000743	MT1.		MAINA	JUMP TO MAINA
1/33/2	000000	61 I I	JMP	0	GO TO PGM START
172274	000000		HALT		
	062703		HALT	#100 PO	
1/3400	000100		ADD	#100,R3	
172404	012701		MOV	8170000 51	AOFT MEMORY OFFICE LANCE
1/3404	160000		NOV	#160000,R1	SET MEMORY CHECK LIMIT
172410	012702		MOV	#/ po	ATDAD HEOTOD A A /
1/3710	000006		HUV	#6,R2	TRAP VECTOR 4 & 6
173414	005012		CLR	/ D2 \	ACID TOAD CTATUS AT LCC /
173414			MOV	(R2) PC,-(R2)	CLR TRAP STATUS AT LOC 6
173420			MOVB	PC, SP	SET TRAP ADDR AT LOC 4
	014304	14.			SET UP STACK OUT OF WAY
173424	005714	1 P.	MOV TST	-(R3),R4 (R4)	GET DEVICE ADDR
173426			BMI	1\$;CHECK IF THERE ;BR IF HSR OUT OF TAPE
270720	100//3		111	1.4	JON IL HOW OUT OF THEE

PROGRAM B

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SCALE REV

A SHEET 3-7

BOOTSTRAP-RL11 MACRO V06-04A 11-AUG-77 00:00 FAGE 5

4.004.00	040740	MCII	DC / D2 \	;TAPE(ERR BIT 15) ;RESET TRAP ADDR AT LOC 4
173430 (173432 (MűV MOV	PC,(R2) #24,R6	SPECIAL ADDR USED AS MASK
	000024	MOV	#2TINO	YOU ECTRE TIESH GOLD TO THE
173436		MOV	R4, -(R1)	; MEM CHK: RDR STAT ADDR MOVED
173440		DIC	CD D1	; SET R1=X7752, MASK IN SP=24
173442	010111	MOV	R1, (R1) (R1), R2 (R4)	STORE OWN ADDR IN POINTER
173444	011102 2\$:	MOV	(R1), R2	GET BYTE POINTER
173446		INC	(R4)	FENABLE READER
173450		1010	\ N /	;TESTN DONE BIT 7 ;WAIT UNTIL READY
173452		BPL MOVB	2(R4),(R2)	THEN PICK IT UP AND STORE IT
	116 4 12 00002	HUVD	21 14 // 1 12 /	THEN TEN TO THE CHARLE TO
	005211	INC	(R1)	;BUMP POINTER
173462	120227	CMPB	(R1) R2,#375	STORED JUMP OFFSET?
1,0402	000375			
173466	001366	BNE	2\$	NOT YET
173470		INCB	(R2)+ -(R2)	; YES, ALL DONE
173472		JMP	-(R2)	GO EXECUTE AS BRANCH
	177560 DEV:	LSR		;LOW SPEED RDR ;HIGH SPEED RDR
173476	1//330 000005 RX:	HSR RESET		RESET BUS
173500		CLR	RO	SET RO TO O
	105714 1\$:	TSTB	(R4)	; WAIT FOR TRANSFER REQ
173506		BEQ	RO (R4) 1\$;NO, BRANCH BACK
173510		MOV	#3,(R4)	;YES, EMPTY BUFF, GO
	000003			
	005714 2\$:	TST	(R4)	; WAIT FOR SOMETHING
173516	001776	BEQ	2\$	NOTHING YET
	100767	BMI	RX (B4)	;ERR: START OVER ;FINISHED TRANSFER?
173522 173524		TSTB BPL	(R4) 3\$;YES, BRANCH
173524 173526		MOVB	2(R4),(R0)+	;NO, PUT DATA INTO MEM
	000002	HOVE	200777007	
173532		BR	2\$	FGET NEXT CHAR
173534		HALT		;EXTRA ROM LOCATION
173536	005000 3\$:	CLR	RO	; PUT O IN RO
173540	000110	JMP	(RO)	START PGM @ O
	000000	HALT		EXTRA ROM LOCATIONS
173544		. WORD	0	; " ;
173546	000000	. WORD	0	
173550		.WORD .WORD	0	, , , , , , , , , , , , , , , , , , , ,
173554		. WORD	ŏ	r 11
173556		. WORD	ŏ	, ,
173560	000000	. WORD	ŏ	, ,
173562	000000	. WORD	0	, 11
173564	000000	. WORD	0	; II
173566		. WORD	0	; II
173570		. WORD	0	
173572	000000	. WORD	0	; !!
173 574 173576		.WORD .WORD	0	EXTRA ROM LOCATIONS
1/32/6	00000	. WOLD	V	FAIRE BONE AND THE
	000001	.END		
		· · -	PROGRAM I	В

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		CODE IDENT NO.	DWG NO.	
9	Α	52648	MA 700596	
1	SCALE	REV	A SHEET 3-8	_

BOOTSTRAP-RL11 MACRO VO6-04A 11-AUG-77 00:00 PAGE 6. SYMBOL TABLE

BC = 172524DEV 173474 DT 173246 173274 LSR = 177560**HSR** = 177550MAIN 173372 MAINA 173300 MT 173340 MT1 RX 173500 **R6** =%000006 R7 =%000007 START 173000 TABLE 173144 TBL1 173164 TKB = 177562= 177560**TPB** = 177566TKS

PS = 177564 . ABS. 173600 000

000000 001
ERRORS DETECTED: 0

FREE CORE: 12981. WORDS

, LP: /NL: SEQ<PERRY1

PROGRAM B

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SIZE CODE IDENT NO.

MA 700596

SCALE REV

A

SHEET 3-9

Appendix A Parts List

PL 700596-100 PL 700596-201 REV A

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SIZE

CODE IDENT NO. DWG NO.

52648

MA 700596

SCALE

REV

SHEET

P	ARTS Plessey Memori			PRETABLE	r. by I.	PARTS LIST NO.		
	ARTS Plessey Memori	ies Incor	porated	TON	M COMEY 2-23-76		SD/ //	REY LOS
ASTY TITLE		San	ta Ana, California	1502	w. Tul 3/1/16	PL 700	3 36 70	00
18	ROARD ASSENIBL	Y		ACSP BIGH	aut E	CODE IDENT NO.	123457	O SH /
	ONI LOADER	<i>,</i> ,		49	AV111 3/3/76	52648	* B	0F 6
	M-RL/II			F-41	20 Mn "	MINCT M		
			~~x	1	3-26-76			
LTR	DE SCRIPTION	DATE	APPROVED	T				
_	RELTOPEDD PER EROSO0155	4-15-76		LTR	DESCRIPTION		DATE	APPROVED
A	INCORP ED 1195			-				
В		7.28.76	73	1				
	INCORP E.O. 1484 18	9-14-77	X14 38	1				
	INICORP E.O. 1534 2B	9-20-71	At 123 W					
0	INCORP EO 2476	9-6-78	XXXXX					
			\\ \	l				
1 1								
					DDOL	DUCTION	DELE	ACE
					- KOL	JUCTION	RELE	A)E
STAT						INTERPRET S	YHBOLS USED AS FOL	LOWS:
OF SHEE		8 9 10	5			A - PURCHESLO (TER B - PARRICATER STEM	0 - Millere film	- BALE TION
OHLL	; <u>«</u> 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	12191	<u> </u>			\$ - \$PECIFICATION OF SOURCE CONTROL ITEM	F • CUSTOMER PURNISHED 21CM	

PARTS Pless	sey l	Memories Incorporat Santa Ana, Cal	ifornia		05	96-100 SH <u>2</u>	RIV LIR
	Į.	CROSS INDEX OF REF	17	DESIGNATIONS TO FINO NO.	1.		
REFERENCE DESIGNATION	1 # R D. D	REFERENCE DESIGNATION	I M M D. D	REFERENCE DESIGNATION	1 # # 0.	REFERENCE DESIGNATION	fi N
TB/	5		22				
	6		23				
	7	R1,15,14	24				
01	8	R2,4	25				
U2	9	R3,10,11,12,13,17	26				
U3,19,20,21,22	10	R5,16	27				
U4,23,24,25,26	11	EG,7,8,9	28			e ⁻¹	
U5	12		29	,			
U6,14	13	EM /	30				1
<i>U7</i>	14		3/				
	15	C1,4,5,9,10,11,13					
	16	C1, 4, 5, 9, 10, 11,13 C14,15,16,17,18,19,20 C21,22,23,24,26,27	32				
	17	C28,29,30,31,33,34				1	
UII	18	C2,3	33				
012,13,17,18	19	C6	34				
U 15	20	C7,8,25,32	35				
016	21	CIR	36				ľ

SIZE	CODE IDENT NO.	DWG	NO.
A	52648		MA 700596
SCALE	REV	A	SHEET A-2

								1	F	П		C/1 US	AGE	
# T # T # C # 4	N O T E	QTY REQ		PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	SPECIFICATION	CODE 10ENT NO.	ZONE	1 N N O. O	S H	C/1 CODE	HAND OH THA	Ä	UNIT COST
		/	ĿΑ	700594-001	P.W.B. ROM LOADER				/	В				
۲			1	1	HANDLE, CARD PULL				2	4				
		4	EΑ	MS16535 -154	RIVET, TUBULAR, OVAL HEAD .123 DIA. X. 188 LONG, AL ALY				3	A				
									4					
$ \mathcal{B} $		1	ΕA	US-2-16 -110-G-B	SOCKET, IG PIN DIP	SCANBE	18677		5	4				
В		/	EΑ	700066 -115	PLUG, ADDRESS STRAPPING				6	B				
									7	L				
		/	EA	5N74123	DUAL RETRIG MONO MULTI W/CLEAR	TEXAS INSTR	01295		8	A				
		/	EA	SN74 H74	DUAL D-TYPE POS EDG- TRIG FIF WIPRESET CLEAR	TEXAS INSTR	01295		9	4	_			
		5	EA	136021-380	QUAD 2-INPUT NOR RECEIVER				10	c				
		5	EA	SN7438	QUAD 2-INPUT POS-NAND BUFFERS W/OC	TEXAS INSTR	01295		//	A				
		/	EA	SN74H76	CLEAR	TEXAS INSTR	01295		12	A				
		2	EA	SN74H40	DUAL 4-INPUT POS- NAND BUFFERS	TEXAS INSTR	01295		13	1				
		/	EA	SN7442	4-LINE-TO-IOLINE DECODERS	TEXAS INSTR	01295		14	14				
D									15	-				
D									16	50				
D									17	10				
		1	EA	100052-001	1024-BIT READ-ONLY MEMORIES				18	3/0				
		4	EA	SN74175	HEX /QUAD D-TYPE F/F W/CLEAR	TEXAS INSTR	01295		19	,4				
		/	EA	5N7485	4-BIT MAGNATUDE COMPARATORS	TEXAS INSTR	01295		20	14				
		1	EA	SN74145	BCD-TO-DEC DECODERS/DEIVERS	TEXAS INSTR	01295		21	A				
									22					
									25					
		3	ΕA	RC07GF 331J	RESISTOR, 3301. ±5%,1/4W	MIL-R-11			24	4				
		z	EΑ	RC07GF 562J	RESISTOR, 5.6K ±5%, 1/4W	MIL-R-II			25	1				
		6	EA	RCOTGF 102J	RESISTOR, 1.0K±5%,1/4W	MIL-R-11			24	Á				

DWG NO. CODE IDENT NO. SIZE

MA 700596 52648 SHEET A-3

1.1	N	1							F	,		C/I US	AGE	, .
	O T E	QTY REQ	/ //H	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	SPECIFICATION	CODE IDENT NO.	ZONE	1 N N O. D	M	C/1 C00F	UNU ON HAND	P A R	UNIT COST
		2	EA	RCOTGF 1511	RESISTOR, 1501 ±5%, 1/4W	MIL-R-II		.,	27	A				
		4	EA	RC07GF 681J	RESISTOR, 680 1 15%,1/4W	MIL-R-II			28	4				
									29					
		/	FΑ	100013-004	RESISTOR, MODULE 330.				30	В				
									31					
		26	EΑ	C069B160 E103 Z	CAPACITOR .01 UF +80-20%, 16 V CAPACITOR 10 PF + 5%, 500 V	SPRAGUE	05571		32	4				
			FΑ	CD15CD 100J03	CAPACITOR/10 PF ± 5%, 500V	CORNELL DUBILIER	93790	, , , , , ,	33	4				
		/	EA	CD15 FD 101 J 03	CAPACITOR 100 PF ± 5%, 500V CAPACITOR 15 UF ± 10%, 20V CAPACITOR 220 PF ± 5%, 500V	CORNELL DUBILIER	93790		34	4				
П		4	ΕA	150D156X 0020B2	CAPACITOR 15 UF #10%, 20V	SPRAGUE	05571		35	4				
		/	EΛ	CD15FD 221J03	CAPACITOR 220 PF ±5%, 500V	CORNELL	93790		36	A			П	
				1.70					37					
					And the second s				38					
						· · · · · · · · · · · · · · · · · · ·			39					
		3	IN	5951	WIRE / 30 AWG SOLID, KYNAR	ALPHA	23172		40	G			П	
		REF		SNG3WRAP3	SOLDER	QQ-5-57/			41	G				
									42					
									43					
		REF		5D700596	SCHEMATIC DIAGRAM ROM LOADER PM-RL/II				44	c			Н	
		REF		TS700596	TEST SPECIFICATION ROM LOADER PM-RL/II				45	c				
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BO	RTS IST ARD A M- R		M	BL	Ϋ,							,		GORI	ON BAILEY 6.11-77 C. Tunkin 7-6-77	PL 700.59 SCOOL IDENT NO. 198 52648 178		SH
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															PRO	DUCTION	l REL	EASE
RE	V REV	LTR	A	A	A		Α	A		T						INTERPRET S	MBOLS USED AS FO	LLOWS:
STAT O SHEE	F			2	3	4	5	6	7	8	9	10				A - PROCHASED TEEN B - FRANCIATED STEN B - SPECIFICATION ON SOURCE ORIGINAL STOR	9 - ALTERED FIEN E - SELECTED ETEN F - CHATCHER FUNNISHED 1988	4 - BACK ITEM

PARTS Pless	ey M	lemories Incorporate Santa Ana, Calif	огпіа		05	96-20.1 SH 2 A
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TBI	5		22			
	6		23			
	7	R1,15,14	24			
UI	8	R2,4	25			
02	9	R3,10,11,12,13,17	26			
U3,19,20,21,22	10	R5,16	27	V		
U4,23,24,25,26	//	R6,7,8,9	28			
U5	12		29			
U6,14	/3	RM I	30			
UT	14		3/			
	15	C1, 4, 5, 9, 10, 11,13				
<i>U9</i>	16	(22,22,23,25,26,27	32			
010	17	C28,29,30,31,33,34		a contract of		
UII	18	C2,3	33			
U12,13,17,18	19	C6	34			
U15		C7,8,25,32	35			
U16	21	CIR	36			

SIZE	CODE IDENT NO.	DWG NO.	
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1:1	NO	QTY	PART OR			cont		f	Īs		C/I US	AGE	
111	Ē	REQ0	IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	SPECIFICATION	CODE 1 DENT NO.	ZONE	I N O O	H	C000	EMY GM KAND	PAR	UNIT COST
		/	70050-1-001	P.W.B. ROM LOADER				1	5				
		2	701339	HANDLE, CAED				2	A				
		4	MS16535 -154	RIVET, TURULAR, OVAL HEAD 123 DIA. X. 188 LONG, AL ALY	2			3	1				
								4					
		1	US-2-16 -110-G-13	SOCKET, IG PIN DIP	SCANBE	18677		ح	4				
		/	700066-207	PLUG, ADDRESS STRAPPING				6	B				
	,							7			•	П	
		/	SN74123	DUAL RETRIG MOND	TEXAS INSTR	21295		8	И				
		/	5N74-H74	DUAL D-TYPE POS EDG-	TEXAS INSTR	01295		9	И				
		5	136021-330	QUAD 2-INPUT NOR RECEIVER				10	C				
		5	SN7433	QUAD 2-INPUT POS-NAND BUFFERS W/OC	TEXAS INSTR	01295		//	4				
	_	/	SN74H76	DUAL J-K F/F/PRESET/ CLEAR	TEXAS INSTR	01295		12	4				
		2	5N74H40	DUAL 4-INPUT POS- NAND BUFFERS	TEXAS INSTR	01295	·	13	4				
		/	SN7442	4-LINE-TO-IOLINE DECODERS	TEXAS INSTR	01295		14	4				
								15					
		/	100015-008	1024-ETT READ-ONLY				16	С				
			100015-007	1094-BIT READ-ONLY MEMOISIES				17	С				
		1	100015-005	1024-BIT READ-ONLY MEMORIES			1	18	c	1			
·		4	SN74175	HEX /QUAD D-TYPE FIF W/CLEAR	TEXAS INSTR	01295		19	4				
		/	5N7485	4-BIT MAGNATUDE COMPARATORS	TEXAS INSTR	01295		20	A				
	_	/	SN74145	BCD-TO-DEC DECODERS/DEIVERS	TEXAS INSTR	01295		21	4				
								22					
								23	Ī			T	
		3	RCOTGF 33/J	RESISTOR, 3301 ±5%,1/4W	MIL-R-11		ŕ	24	A			1	一
		2	RC07GF 562J	RESISTOR, 5.6K ±5%, 1/4W	MIL-R-II		- /	?5	1	1			
		6	EC07GF 102J	RESISTOR, 1.0K±5%,1/4W	MIL-R-II			26	4	T			

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F:===								F	.1		C/1 US	AGE	
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	N O T E	QTY REQO	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	SPECIFICATION	CODE 10ENT NO.	ZONE	I M N O. O	M A	C/1	THY ON HAND	.42	ENTY COST
		2	RCOTGF 151U	RESISTOR, 1501. ±5%,1/4W	MIL-R-II			27	A				
		4	RCOTGF 681J	RESISTOR, 6801. ±5%,1/4W	MIL-R-II			28	٨				
							٠	29					
		1	100013-004	RESISTOR, MODULE 3301				30	B				
								31					
		26	CO69B160 E103 #	CAFACITOR/+80-20%,16V	SPRAGUE	05571		32	4				·
		2	CD15CD 100J03	CAFACITOR +80-20%, 16'V CAPACITOR +5%, 500V	CORNELL DUBILIER	93790		33	h				
		/	CD15 FD 101 JO2	CAPACITOR 100 PF ±5%,500V	CORNELL DUBILIER	93790		34	1				
		.4	150D156X 0020B2	CALACITUR #10%, 20V	SPRAGUE	05571		35	14				
		1	CD15FD 221J03	CAPACITOR 220 PF	CORNELL DUBILIER	23790		36	1			\downarrow	
								37	1.				
								38					
								39	1				
		AR	5951	WIRE / 30 AWG SOLID, KYNAR	ALPHA WIRE	23172		40	G				
		AR	SN63WRAP3	SOLDER	QQ-5-571			41	6				
								4,2	2		<u></u>		
								43	3				
		REF	50700596	SCHEMATIC DIAGRAM ROM LOADER PM-RL/II				44	2 C				
		REF	75700596 -201	TEST SPECIFICATION ROM LOADER PM-RL/II				45					
								10	3				
T								47	7				
		,						43	3				
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Appendix B Assembly Drawing

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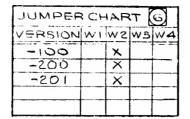
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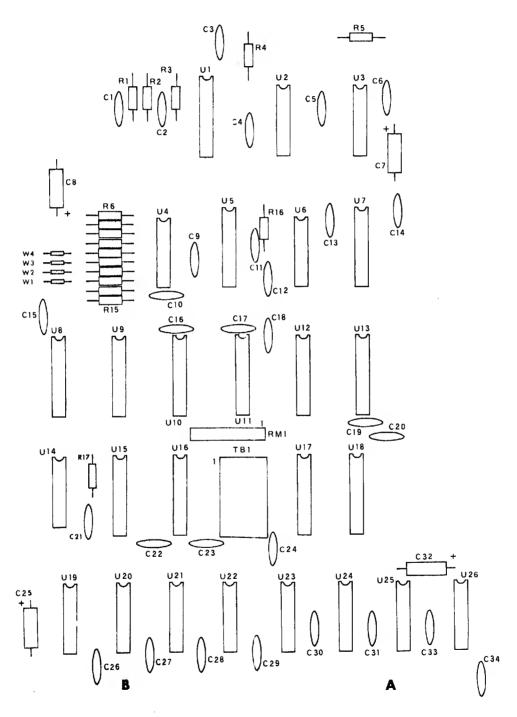
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Appendix C Schematic Diagrams

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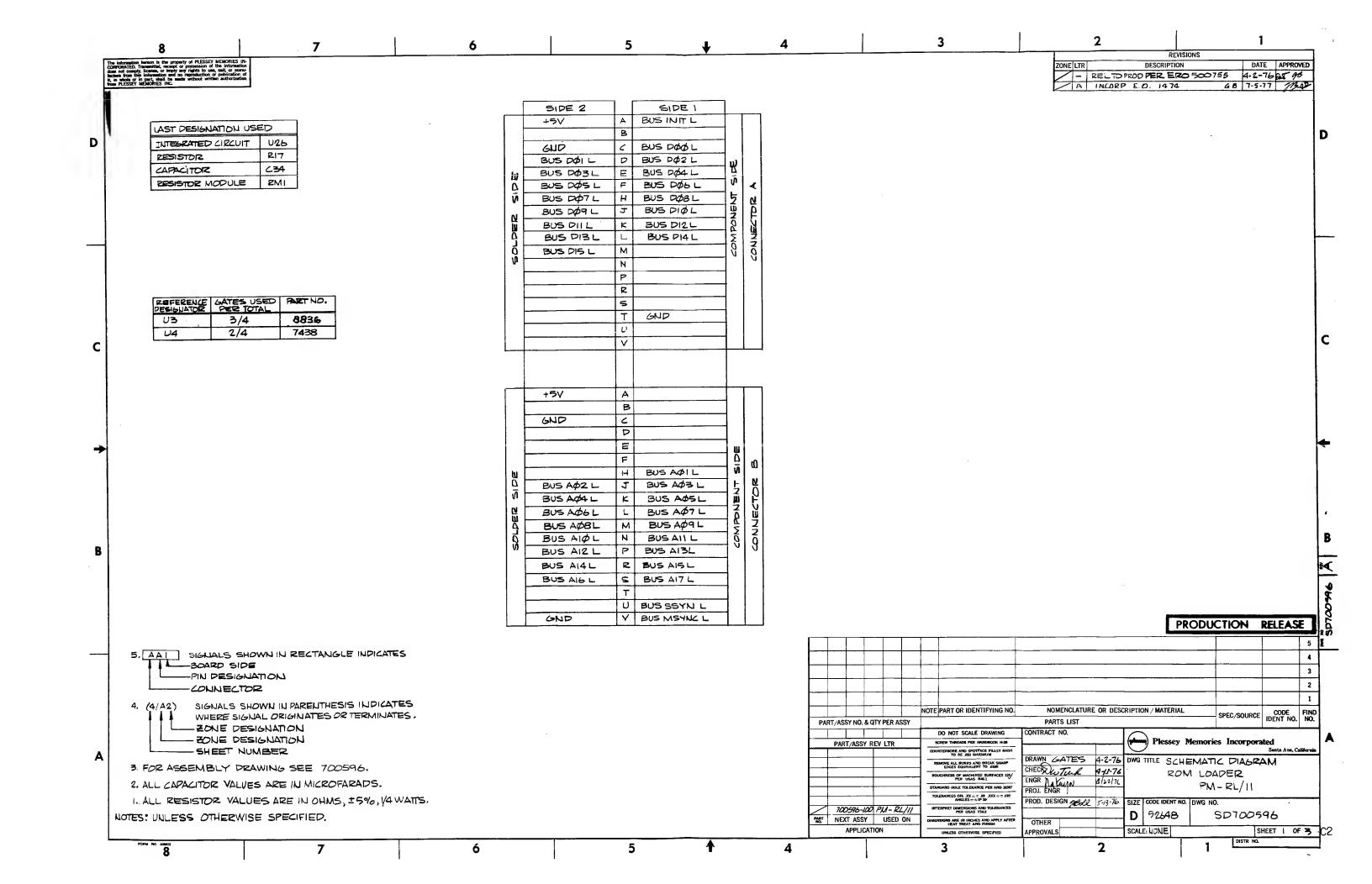
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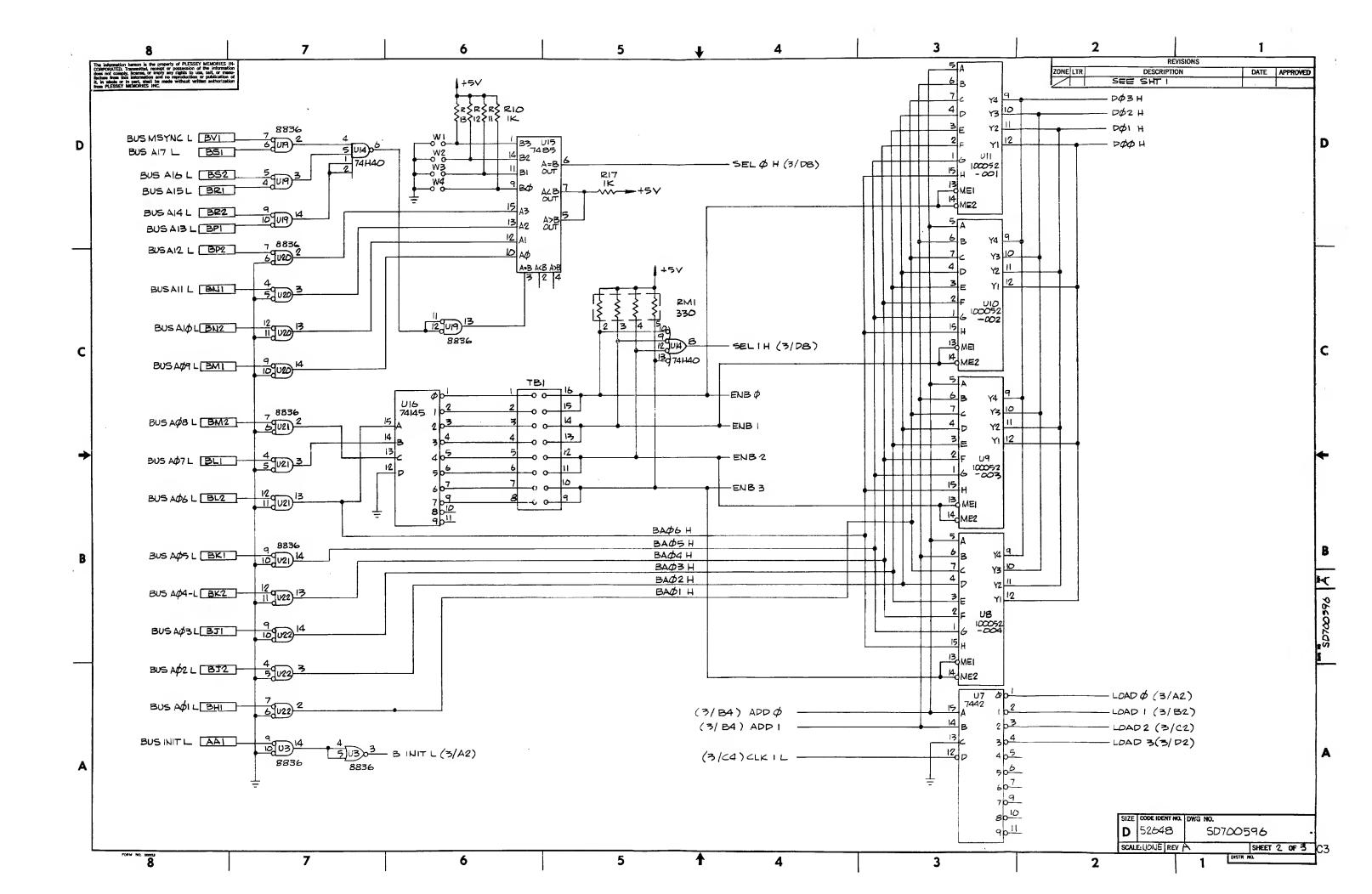
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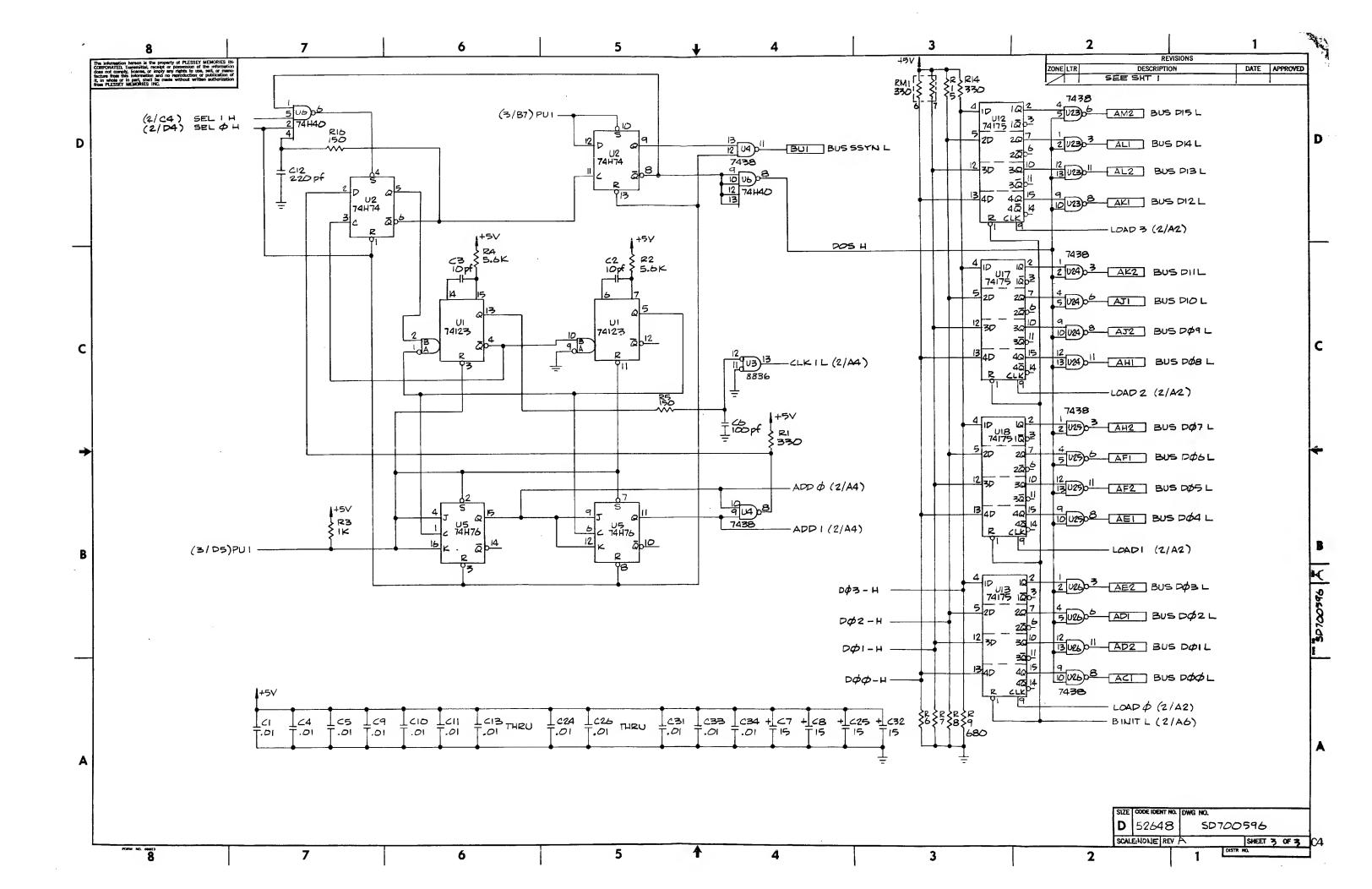
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